

Input Specifications

Parameter	Model	Min.	Typ.	Max.	Unit
Input Surge Voltage (1 sec. max.)	5V Input Models	-0.7	---	9	VDC
	12V Input Models	-0.7	---	18	
	15V Input Models	-0.7	---	20	
	24V Input Models	-0.7	---	30	
Input Voltage Range	5V Input Models	4.5	5	5.5	
	12V Input Models	10.8	12	13.2	
	15V Input Models	13.5	15	16.5	
	24V Input Models	21.6	24	26.4	
Short Circuit Input Power	All Models	---	---	1000	mW
Input Filter		Internal Capacitor			

Output Specifications

Parameter	Conditions	Min.	Typ.	Max.	Unit
Output Voltage Setting Accuracy		---	---	± 5.0	%Vnom.
Output Voltage Balance	Dual Output, Balanced Loads	---	± 0.1	± 1.0	%
Line Regulation	For Vin Change of 1%	---	± 1.2	---	%
Load Regulation	Io=20% to 100%	See Model Selection Guide			
Ripple & Noise	0-20MHz Bandwidth	---	---	100	mV P-P
Temperature Coefficient		---	± 0.01	± 0.02	%/°C
Short Circuit Protection	Continuous, Automatic Recovery				

Isolation, Safety Standards

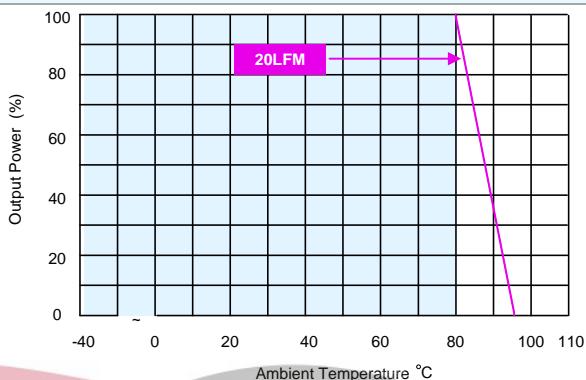
Parameter	Conditions	Min.	Typ.	Max.	Unit
I/O Isolation Voltage	Rated for 60 seconds	5200	---	---	VDC
	Tested for 1 second	5700	---	---	VDC
I/O Isolation Resistance	500 VDC	10	---	---	GΩ
I/O Isolation Capacitance	100KHz, 1V	---	7	---	pF
Common Mode Transient Immunity		15	---	---	kV/μs
Safety Approvals	UL/cUL 60950-1 recognition (CSA certificate), IEC/EN 60950-1 (CB-report)				

General Specifications

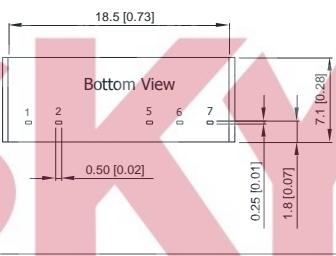
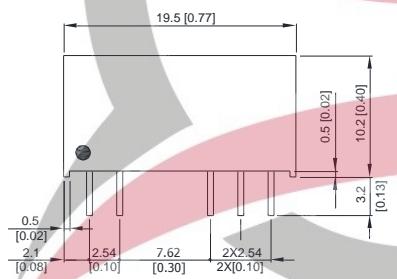
Parameter	Conditions	Min.	Typ.	Max.	Unit
Switching Frequency		---	100	---	KHz
MTBF(calculated)	MIL-HDBK-217F@25°C, Ground Benign	1,109,000	---	---	Hours

Environmental Specifications

Parameter	Conditions	Min.	Max.	Unit
Operating Ambient Temperature Range (See Power Derating Curve)		-40	+85	°C
Case Temperature		---	+95	°C
Storage Temperature Range		-55	+125	°C
Humidity (non condensing)		---	95	% rel. H
Lead Temperature (1.5mm from case for 10Sec.)		---	260	°C

Power Derating Curve

Notes

- 1 Specifications typical at $T_a=+25^\circ\text{C}$, resistive load, nominal input voltage and rated output current unless otherwise noted.
- 2 These power converters require a minimum output loading to maintain specified regulation, operation under no-load conditions will not damage these modules; however they may not meet all specifications listed.
- 3 We recommend to protect the converter by a slow blow fuse in the input supply line.
- 4 Other input and output voltage may be available, please contact factory.
- 5 Specifications are subject to change without notice.

Package Specifications
Mechanical Dimensions

Pin Connections

Pin	Single Output	Dual Output
1	+Vin	+Vin
2	-Vin	-Vin
5	-Vout	-Vout
6	No Pin	Common
7	+Vout	+Vout

► All dimensions in mm (inches)
 ► Tolerance: $X.X \pm 0.5$ ($X.XX \pm 0.02$)
 $X.XX \pm 0.25$ ($X.XXX \pm 0.01$)
 ► Pins ± 0.05 (± 0.002)

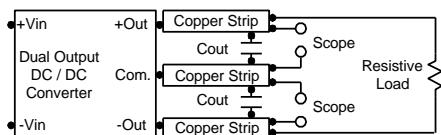
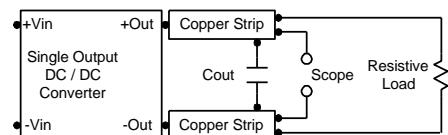
Physical Characteristics

Case Size	: 19.5x7.1x10.2mm (0.77x0.28x0.40 inches)
Case Material	: Non-Conductive Black Plastic (flammability to UL 94V-0 rated)
Pin Material	: Tinned Copper
Weight	: 2.4g

Test Setup

Peak-to-Peak Output Noise Measurement Test

Use a C_{out} 0.33μF ceramic capacitor. Scope measurement should be made by using a BNC socket, measurement bandwidth is 0-20 MHz. Position the load between 50 mm and 75 mm from the DC/DC Converter.



Technical Notes

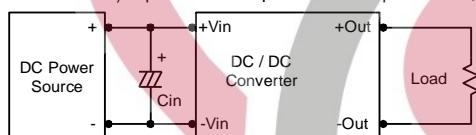
Maximum Capacitive Load

The MAEU02-HI series has limitation of maximum connected capacitance at the output. The power module may be operated in current limiting mode during start-up, affecting the ramp-up and the startup time. The maximum capacitance can be found in the data sheet.

Input Source Impedance

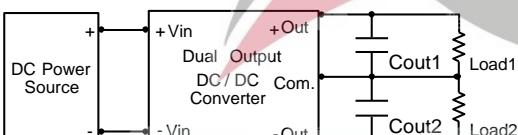
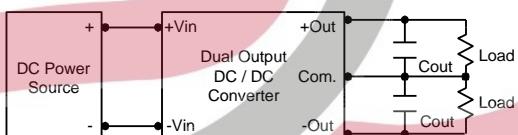
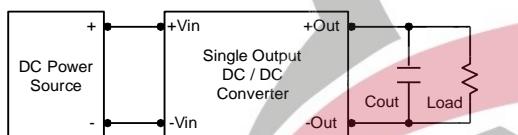
The power module should be connected to a low ac-impedance input source. Highly inductive source impedances can affect the stability of the power module. In applications where power is supplied over long lines and output loading is high, it may be necessary to use a capacitor at the input to ensure startup.

Capacitor mounted close to the power module helps ensure stability of the unit, it is recommended to use a good quality low Equivalent Series Resistance (ESR < 1.0Ω at 100 KHz) capacitor of a 2.2μF for the 5V input devices, a 1.0μF for the 12V,15V input devices and a 0.47μF for the 24V devices.



Output Ripple Reduction

A good quality low ESR capacitor placed as close as practicable across the load will give the best ripple and noise performance. To reduce output ripple, it is recommended to use 1.0μF capacitors at the output.



Thermal Considerations

Many conditions affect the thermal performance of the power module, such as orientation, airflow over the module and board spacing. To avoid exceeding the maximum temperature rating of the components inside the power module, the case temperature must be kept below 95°C. The derating curves are determined from measurements obtained in a test setup.

